

ReConFig 2017

The 12th edition of the International Conference on ReConFigurable Computing and FPGAs (ReConFig17), organized by the National Institute for Astrophysics, Optics and Electronics (INAOE) of Mexico in collaboration with Virginia Tech, USA, and the University of North Carolina at Charlotte, USA., was held at the Laguna Conference Center of the Grand Park Royal Cancun Caribe at Cancun, Mexico on December 4-6, 2017. .

ReConFig seeks to promote the use of reconfigurable computing and FPGA technology in applications, research and education. The conference includes topics such as hardware architectures and devices, high-performance computing architectures, embedded real-time reconfigurable systems and multiprocessor systems on chip.

For ReConFig17, submissions were invited for the general track as well as for the following special tracks on reconfigurable computing: High Performance Reconfigurable Computing, Multiprocessor Systems and Networks on Chip, Productivity Environments and High-Level Languages, Reconfigurable Computing for Signal Processing and Space Applications, Reconfigurable Computing for Security and Cryptography, and Reconfigurable Computing for Networks and Communications. In this year's conference 49 papers by authors from 17 countries were selected and presented.

A PhD Forum was held to provide PhD candidates feedback from experts on their ideas, developments and preliminary results. A demo and project presentation session was held during the welcome reception. This session was open to all attendees to present projects and research ideas or results and were not necessarily be related to papers presented during the technical sessions.

The first keynote speaker was John Watson of Micron, USA, that gave a talk titled *Education is not learning facts, but training the mind to think*. The second keynote speaker was Christian Pless of Paderborn University, Germany, titled *FPGA-accelerated High-Performance Computing – Close to Breakthrough or Pipedream?*.

We are very grateful to the members of the organizing committee whose hard work made the vision of the ReConFig 2017 a reality. We extend our most sincere thanks the program committee and reviewers for their time and effort in the review process and for their constructive feedback to authors. We acknowledge the support of the IEEE CAS Society. Finally, we thank all authors who contributed to this conference by sharing their novel ideas, methods and results with the research community in the growing and developing field of Reconfigurable Computing and FPGA technology.

René Cumplido, INAOE, Mexico
General Chairs, ReConFig17

Peter Athanas, Virginia Tech, USA
Ron Sass, UNC Charlotte, USA
Program Co-chairs, ReConFig17



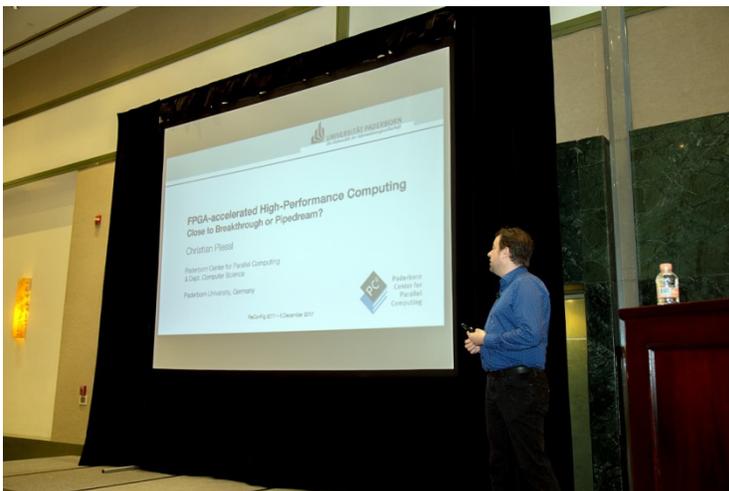
ReConFig17 Group Photo



ReConFig17 Technical Session



ReConfig17 Keynote Speaker – John Watson, Micron, USA



ReConFig17 Keynote Speaker - Christian Pless, Paderborn University, Germany



ReConFig17, Poster Session



ReConFig17 Conference Dinner