

# ISCAS Report

## Conference Committee

### General Chair

Nobuo Fujii, Tokyo Inst. of Tech., Japan

### Technical Program Committee

#### Co-Chairs:

Rokuya Ishii, Yokohama Nat'l Univ., Japan

Ljiljana Trajkovic, Simon Fraser Univ., Canada

#### Vice Chair:

Shin'ichi Oishi, Waseda Univ., Japan

#### Secretary:

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### Publications Chair

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Maciej J. Ogorzalek, AGH Univ. of Sci. & Tech., Poland

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Yrjo Neuvo, Nokia, Finland

## **\_Technical Program Committee**

### (1) ISCAS 2005 Technical Program Committee Members

#### Co-chairs

Rokuya Ishii (Yokohama National University, Japan)

Ljiljana Trajkovic (Simon Fraser University, Canada)

#### Vice chair

Shin'ichi Oishi (Waseda University, Japan)

#### Secretary

Koichi Ichige (Yokohama National University, Japan)

#### Student Paper Contest Co-Chairs for Financial Support

Masaru Kamada (Ibaraki University, Japan)

Takehiko Adachi (Yokohama National University, Japan)

Antonio Torralba (University of Seville, Spain)

#### Trackchairs

##### 1. Analog Signal Processing

Akira Hyogo (Science University of Tokyo, Japan)

Wouter A Serdijn (Delft University of Technology, The Netherlands)

Igor Filanovsky (University of Alberta, Canada)

Un-Ku Moon (Oregon State University, U.S.A.)

##### 2. Biomedical Circuits and Systems

Kaoru Arakawa (Meiji University, Japan)

Tor Sverre Lande (University of Oslo, Finland)

##### 3. Blind Signal Processing

Yujiro Inouye (Shimane University, Japan)

Noboru Nakasako (Kinki University, Japan)

##### 4. Cellular Neural Networks and Array Computing

Mamoru Tanaka (Sophia University, Japan)

Bertram Shi (Hong Kong University of Science and Technology, Hong Kong)

##### 5. Circuits and Systems for Communications

Isao Yamada (Tokyo Institute of Technology, Japan)

Nam Ling (Santa Clara University, U.S.A.)

Gerald Sobelman (University of Minnesota, U.S.A.)

##### 6. Computer-Aided Network Design

Shoji Kawahito (Shizuoka University, Japan)

Sung Kyu Lim (Georgia Institute of Technology, U.S.A.)

##### 7. Digital Signal Processing

Akira Taguchi (Musashi Institute of Technology, Japan)

H. K. Kwan (University of Windsor, Canada)

Tapio Saramaki (Tampere University of Technology, Finland)

##### 8. Graph Theory and Computing

Kunihiko Hiraishi (JAIST, Japan)

K. Thulasiraman (University of Oklahoma, U.S.A.)

##### 9. Life-Science Systems and Applications

Hitoshi Kiya (Tokyo Metropolitan University, Japan)

Stephen Wong (Harvard Medical School, U.S.A.)

##### 10. Multimedia Systems and Applications

Ichiro Kuroda (NEC Electronics, Japan)

Wai-Chi Fang (California Institute of Technology, U.S.A.)



Track		Submitted papers	Accepted papers		Oral session		Poster session	
1	Analog Signal Processing	400	256	64%	144	36%	112	28%
2	Biomedical Circuits and Systems	50	30	60%	18	36%	12	24%
3	Blind Signal Processing	38	28	74%	12	32%	16	42%
4	Cellular Neural Networks and Array Computing	28	20	71%	12	43%	8	29%
5	Circuits and Systems for Communications	229	146	64%	90	39%	56	24%
6	Computer-Aided Network Design	129	89	69%	54	42%	35	27%
7	Digital Signal Processing	265	180	68%	108	41%	72	27%
8	Graph Theory and Computing	28	20	71%	12	43%	8	29%
9	Life-Science Systems and Applications	13	9	69%	9	69%	0	0%
10	Multimedia Systems and Applications	132	94	71%	54	41%	40	30%
11	Nanoelectronics and Gigascale System	17	12	71%	6	35%	6	35%
12	Neural Systems and Applications	52	36	69%	24	46%	12	23%
13	Nonlinear Circuits and Systems	147	93	63%	60	41%	33	22%
14	Power Systems and Power Electronic Circuits	90	62	69%	48	53%	14	16%
15	Sensory Systems	55	40	73%	24	44%	16	29%
16	Visual Signal Processing & Communications	162	115	71%	69	43%	46	28%
17	VLSI Systems and Applications	317	210	66%	144	45%	66	21%
18	Consumer Technologies and Applications	33	20	61%	12	36%	8	24%
19	Special Session	166	162	98%	154	93%	8	5%
-	Total	2351	1622	69%	1054	45%	568	24%

### (3) Conference Schedule

	Sun, May 22nd	Mon, May 23rd	Tue, May 24th
9:00-11:00	-	Tutorial 9:00 - 12:30	Opening Ceremony Two Keynote Talks
11:00-11:10	-		Coffee Break
11:10-13:10	-		Oral Session (16) Poster Session (7)
13:10-14:00	-		Lunch
14:00-16:00	Committee Meeting 14:00-16:30	13:30 - 17:30 (13:30-17:00)	Oral Session (16) Poster Session (7)
16:00-16:10			Coffee Break
16:10-18:10			Oral Session (16) Poster Session (7)
18:10-18:30	-	Welcome Reception (18:00-)	Break
-	-		Invited Talk (18:30-19:30)

	Wed, May 25th	Thu, May 26th	Fri, May 27th
9:00-11:00	Oral Session (16) Poster Session (7)	Oral Session (16) Poster Session (7)	IEEE Circuits and Systems Society Workshop
11:00-11:10	Coffee Break	Coffee Break	
11:10-13:10	Oral Session (16) Poster Session (7)	Oral Session (16) Poster Session (7)	
13:10-14:00	Lunch	Lunch	
14:00-16:00	Oral Session (16) Poster Session (7)	Oral Session (16) Poster Session (7)	
16:00-16:10	Coffee Break	Coffee Break	
16:10-18:10	Oral Session (16) Poster Session (7)	Oral Session (16) Poster Session (7)	
18:10-18:30	Break	Break	
–	Banquet (19:00-22:00)	Farewell Party (18:30- )	–

(4) Statistics of Financial Support Recipients

Iran	3
Brazil	2
China	2
Korea	2
Hong Kong	2
Singapore	2
Taiwan	2
Australia	1
Croatia	1
India	1
Israel	1
Macao	1
Malaysia	1
Poland	1
Trinidad & Tobago	1
Spain	1
Total	24

(5) Oral Session Statistics

Track		Papers	Sessions	Audiences (average)
1	Analog Signal Processing	256	24	36
2	Biomedical Circuits and Systems	30	3	54
3	Blind Signal Processing	28	2	34
4	Cellular Neural Networks and Array Computing	20	2	34
5	Circuits and Systems for Communications	146	15	22
6	Computer-Aided Network Design	89	9	25
7	Digital Signal Processing	180	19	22
8	Graph Theory and Computing	20	2	19
9	Life-Science Systems and Applications	9	2	20
10	Multimedia Systems and Applications	94	9	20
11	Nanoelectronics and Gigascale System	12	1	25
12	Neural Systems and Applications	36	4	20
13	Nonlinear Circuits and Systems	93	10	24
14	Power Systems and Power Electronic Circuits	62	8	22
15	Sensory Systems	40	4	30
16	Visual Signal Processing & Communications	115	11	30
17	VLSI Systems and Applications	210	24	23
18	Consumer Technologies and Applications	20	2	20
19	Special Session	162	26	29
-	Total	1622	177	

(6) Tutorial Session Statistics

	Session Title	
1	High Level Verification Techniques for System LSI Designs	12
2	RFID Technology World, from the Production to the Application System	27
3	Embedded CPU Core in SOC Design Field	18
4	Multi-Standard Adaptive Circuits and Systems for Wireless Communications	47
5	Digital Filters on Silicon: Design and Implementation	20
6	Advanced Circuit Techniques to Implement Biomedical Implantable Microsystems	49
8	Design, Implementation and Control for High-Performance MEMS	14
9	Clocking of Digital Systems for High-Performance and Low-Power	24
10	Ultra Wide Band Radio in Distributed Wireless Networks	50
11	Emerging Video Understanding Technologies and Applications	23
12	Circuit and Systems for Lab-on-Chip Integration	34
13	High Performance/Low Power Memory Trends and I/O Interfaces	21
	Total	339

(7) Demographic Data of Accepted Papers

Region	Papers	Region	Papers
Argentina	3	Jordan	1

Australia	23	Korea, Republic of	47
Austria	6	Macau	4
Bangladesh	3	Malaysia	1
Belgium	6	Mexico	3
Brazil	19	Netherlands	17
Bulgaria	1	New Zealand	1
Canada	119	Norway	2
China	94	Poland	3
Colombia	1	Portugal	7
Croatia	3	Romania	1
Denmark	3	Russian Federation	1
Egypt	4	Saudi Arabia	2
Finland	39	Singapore	92
France	16	Spain	35
France, Metropolitan	1	Sweden	25
Germany	28	Switzerland	8
Greece	12	Taiwan	207
Hong Kong	55	Tanzania, United Republic of	1
Hungary	2	Thailand	14
Iceland	1	Trinidad and Tobago	1
India	17	Turkey	5
Iran (Islamic Republic of)	23	United Arab Emirates	3
Ireland	2	United Kingdom	64
Israel	5	U.S.A.	326
Italy	66	Uruguay	1
Japan	198	Total	1622

## • Registrations

The number of conference participants is 1,558, including 21 free invited participants. Breakdowns are listed below.

IEEE/IEICE Members (Advance)	966	@ 63,000 JPY
IEEE/IEICE Members (On-Site)	60	@ 88,000 JPY
IEEE LIFE Members(Advance)	10	@ 28,000 JPY
IEEE LIFE Members(On-Site)	2	@ 35,000 JPY
Non-Members (Advance)	156	@ 77,000 JPY
Non-Members (On-Site)	22	@ 88,000 JPY
IEEE/IEICE Student Members (Advance)	187	@ 28,000 JPY
IEEE/IEICE Student Members (On-Site)	19	@ 35,000 JPY
Student Non-Members (Advance)	102	@ 35,000 JPY
Student Non-Members (On-Site)	13	@ 50,000 JPY
Invited	21	

## Demographic Data

Region	Participants	Members
Argentina	2	2
Australia	17	14
Austria	6	2
Belgium	5	4
Brazil	16	10

Canada	95	71
China	66	36
Columbia	1	0
Croatia	3	2
Denmark	4	3
Egypt	2	1
Finland	38	19
France	16	9
Germany	30	18
Greece	9	9
Hong Kong	38	19
Hungary	3	3
Iceland	1	1
India	14	10
Iran	13	8
Ireland	3	3
Israel	7	4
Italy	47	24
Japan	387	268
Jordan	1	1
Korea	63	30
Macau	5	1
Malaysia	2	1
Mexico	3	3
Norway	4	3
Poland	3	3
Portugal	7	4
Saudi Arabia	1	1
Singapore	46	34
Spain	28	18
Sweden	27	17
Switzerland	14	12
Taiwan	205	120
Thailand	11	6
The Netherlands	21	9
Trinidad	1	1
Turkey	7	5

U.A.E.	3	3
U.K.	53	30
U.S.A.	230	184
Total	1558	1026

▪ **Printing matters**

Alliance Management Group (Indiana, USA) produced:  
 2000 copies of Conference Guide book (326 pages),  
 2000 copies of Conference-at-a-glance pamphlet,  
 2000 copies of CD-ROM (master creation, duplication, tray & disk artwork)  
 including 15 sets of CD-ROM for IEEE Headquarter.

▪ **Publicity**

ISCAS 2005 Poster (A2)  
 ISCAS 2005 Postcard  
 Exhibit booth at ISCAS 2004  
 ISCAS 2005 Call for Paper and Call for Participation  
 Information posted on a Web site (URL: [www.iscas05.org](http://www.iscas05.org))  
 Circuits and Systems Magazine:  
 CFP and the invitation card published in the First Quarter 2005  
 "Conference Highlight" article published in IEEE Circuits and Devices Magazine, March 2005  
 ieeenotice (CFPapers & CFParticipation) sent to all members of the CAS Society

▪ **Local matters**

Hotel room pickups: 582 rooms  
 Student staffs  
 Kobe University: 23  
 University of Hyogo: 6  
 Kwansai Gakuin University: 11  
 Osaka University: 11  
 Kyoto University: 2

▪ **Exhibitors**

1	ISCAS2006
2	John Wiley & Sons Ltd.
3	Springer
4	McGraw-Hill Education Japan
5	Elsevier

Technical Visits

region	participants
Australia	1
Belgium	1
Canada	5
Croatia	1
Greece	1
Japan	15
Singapore	2
India	1

The Netherlands	1
U.S.A.	2
Total	30