



2017

Design Test Verification and EDA workshop

**DTVEDA**

Financially sponsored by the 2017 Outreach Initiative of the IEEE Circuits and Systems Society

**July 6<sup>th</sup> and 7<sup>th</sup>, 2017**

**09.00 - 18.00**

**Domotel Xenia, Volos**

**Greece**

**Organizers and Workshop Chairs:**

**Fotis Plessas,**

University of Thessaly, Greece

**Thanos Kakarountas,**

University of Thessaly, Greece

**No Fees**

**For details please visit:**

[dtveda.e-ce.uth.gr](http://dtveda.e-ce.uth.gr)

**Important Dates**

**Registration deadline:**

June 30th, 2017

**Poster abstract submission deadline:**

June 15th, 2017 at 11:59PM (EEST)

**Thesis submission deadline:**

June 24th, 2017 at 11:59PM (EEST)

**DTVEDA 2017** is initiated to provide a common forum for academic and industry researchers for meeting and exploring new avenues of research. The workshop will not only provide the opportunity for face-to-face interaction among researchers, scientists, and development professionals, but also aims to attract outstanding international speakers.

The program will include 3 invited keynote speeches (international participation), whereas a number of about 10 talks will be given, with participation from industry as well as academia, ranging from premier local academic institutes of Greece to international independents.

**Technical Areas**

- Analog, Digital, Mixed Signal, and RF Design
- Circuit and system techniques for energy harvesting/delivery/management
- EDA
- Verification
- Embedded systems
- FPGAs & HDLs
- Testing
- System-on-chip design and verification methodologies

This year, emphasis will be given to IoT and security projects and applications.

