

About the Institute

Madanapalle Institute of Technology & Science (UGC-Autonomous Institution) popularly known as MITS, was established in 1998 in the picturesque and pleasant environment of Madanapalle and is ideally located on a sprawling 26.17 acre campus on Madanapalle - Ananthapuramu Highway (NH-205) near Angallu, about 10 km away from Madanapalle and 120 km from Bangalore Airport. MITS was originates under the auspices of Ratakonda Ranga Reddy Educational Academy under the proactive leadership of Dr. N. Vijaya Bhaskar Choudary, Ph.D., Secretary & Correspondent. It offers 11 Under Graduate Engineering Programmes, and 2 Post-Graduate Programmes (MBA and MCA). It has benchmarked its curriculum and teaching methodologies with premier Indian Institutes to maintain academic excellence. It is a NAAC 'A+' accredited institution and all the eligible programmes have been accredited by NBA, MITS is ranked among the 251-300 band of NIRF - 2022 Ranking. MITS has entered into MoUs with 17 renowned universities across the world. 52 B.Tech students got selected for University Innovation Fellowship program (UIF) of Stanford University- USA and many students have completed their internships in Taiwan, Japan, Germany, Finland, Russia, Dubai and Singapore. The Institute has around 6,500 students, 363 Teaching Faculty, among which 188 Faculty members are with Ph.D qualification from many renowned Indian and foreign Universities. In addition to these resources, the latest inclusion is MITS-Community Radio 90.8 which is established to foster students' involvement and participation to provide the most recent updates of Science and Technological advancements to the community.

About the Seasonal School

School Title: Seasonal School on "Neuromorphic Computing and Logic In Memory Computing Using Non Volatile Memory Devices"

Description of the seasonal School

This seasonal school covers variety of topics related to fundamentals, ongoing research and future trends related to Non-Volatile Memories for logic in Memory computing as well as Neuromorphic Computing Applications. Experts from National University of Singapore, Nanyang Technological University- Singapore, Fraunhofer IPMS-Germany, CNRS France, University of Tennessee-United Kingdom, University of Sheffield-United Kingdom, Queen's University-Canada, University of Edinburgh- United Kingdom, IBM Research Zurich. - Switzerland and Indian Institute of Technology Kanpur - India.

Contact Details

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Organizing Committee

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Secretary & Correspondent

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Executive Director

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Dr. A.V. Pavan Kumar, EEE HOD
Dr. C. Kumar, IEEE Coordinator

Registration Details

Registration Fees (for Students, Researchers, and Faculty Members), Including the Discount

for CAS Society Members:

Rs.400 for IEEE CAS Members.

Rs.500 for IEEE Members.

Rs.600 for Non-IEEE Members.

Last Date for Registration : 20/09/2024

Registration and payment Link

<https://forms.gle/ZDkS1EbuyFsxd9Fq6>

Seasonal School Website:

www.mits.ac.in/sscas



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(UGC-AUTONOMOUS INSTITUTION)

Madanapalle-517325, Annamayya Dist., Andhra Pradesh

IEEE CAS SEASONAL SCHOOL ON
"NEUROMORPHIC COMPUTING AND
LOGIC IN MEMORY COMPUTING USING
NON-VOLATILE MEMORY DEVICES"

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SEASONAL SCHOOL 2024

23-25 SEPTEMBER 2024

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EMINENT SPEAKERS



Dr Kim Tae Hyung
Nanyang Technological University
Singapore



Dr FONG, Xuanyao Kelvin
National University of Singapore
Singapore



Dr. Thomas Kämpfe
Group Manager Integrated RF & AI
Fraunhofer IPMS, Germany



Dr Damien QUERLIOZ
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Dr. Merlyne De Souza
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Topics to be covered

- RRAM Based Logic in Memory Computing
- STT MRAM based Logic in Memory Computing
- FEFET based in Memory Computing
- Neuromorphic computing with nanoscale spintronic oscillators
- Energy Efficient Neromorphic Devices, Circuits and Systems based on 3D- NAND Flash
- Spike Based Neuromorphic Computing for Next Generation Computer Vision
- Neuristor for Advanced Neuromorphic Intelligent Systems
- Neurmorphic Photonics
- Digital In-Memory Stochastic Computing Architecture for Vector-Matrix Multiplication
- Neuromorphic Computing using Ferroelectric Based Devices

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