IEEE ISICAS 2025

OCTOBER 24-26, 2025
HYATT REGENCY, QINGDAO, CHINA

IEEE INTERNATIONAL SYMPOSIUM ON INTEGRATED CIRCUITS AND SYSTEMS

WELCOME TO QINGDAO

SCOPE

The **International Symposium on Integrated Circuits and Systems (ISICAS)** invites high-quality submissions presenting original work in the areas of integrated circuits and systems. Please note that, in addition to the IEEE ISICAS special section requirements for TCAS-I, TCAS-II, OJCAS, TVLSI, TCAS-AI, and TBioCAS, manuscripts on analog and mixed signal, nonlinear, communications, and power circuits must have an on-chip implementation and measured silicon results. Manuscripts on digital circuits and systems and VLSI must have at least FPGA verification. Submissions not compliant with the above requirements will not be reviewed and will be returned to the authors. Extended versions of previously published recent conference papers with significant additional materials are also welcomed.

All accepted papers will be published in Special Issues of TCAS-I, TCAS-II, TVLSI, TBioCAS, TCASAI, and OJCAS shortly after the results are presented at ISICAS 2025.

PAPER SUBMISSION

All submitted manuscripts must conform to the formatting and page count limit rules set by TCAS-I, TCAS-II, TVLSI, TBioCAS, TCASAI, and OJCAS. The authors of accepted manuscripts wil be asked to present their work (Oral and Poster) at the Conference, Qingdao, China. Note that there will be attractive prizes for Best Poster and Live Demo.

More submission details could be found in the website link: http://2025.ieee-isicas.org

IMPORTANT DATES

PAPER SUBMISSION DEADLINE:
February 20, 2025

NOTIFICATION OF ACCEPTANCE:
July 10, 2025

REGISTRATION STARTS:
July 10, 2025

CONFERENCE DATE:
October 24-26, 2025

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