

The 8th IEEE International Conference on Artificial Intelligence Circuits and Systems



September, 14-16, 2026



HA LONG BAY, VIETNAM



AICAS 2026: AI Circuits and Systems for a Sustainable Future

ABOUT IEEE AICAS

Artificial Intelligence is driving a revolution in information technology and many other industries. New computing paradigms are required to support the emerging AI algorithms and applications running on various platforms, from edge devices to cloud servers and from circuit to system levels. Addressing this challenge and opportunity, the AICAS conference has been established as the premier conference of the IEEE Circuits and Systems Society to promote and facilitate state-of-the-art research, innovation, and development activities at the frontiers of artificial intelligence circuits and systems.

HA LONG BAY, VIETNAM

AICAS 2026 will be held in Ha Long Bay, a breathtaking natural wonder that captivates visitors with its beauty and mystique. Famous for its emerald waters and towering limestone pillars, this UNESCO World Heritage site offers a picturesque landscape that feels like something straight out of a dream. With about 2,000 islands and islets scattered across its vast expanse, Ha Long Bay is a haven with the most stunning seascapes in the world.

PAPER SUBMISSION

Prospective authors are invited to submit full-length papers of a maximum of 5 pages anonymously via the conference website. Four pages are for technical content, and an optional fifth page contains only references.

Proposals for special sessions, live demos, and tutorials are welcome. Submitted papers must describe original work not submitted to other journals or conferences. Authors are expected to present their papers in person at the conference. While the conference proceedings will be submitted to the IEEE Xplore® digital library, the best papers will be recommended to the IEEE Transactions on Circuits and Systems for AI or IEEE Transactions on VLSI Systems for a Special Section.

TOPICS OF INTEREST

- ▶ In-memory computing
- ▶ Circuits and systems for AI
- ▶ Architecture for AI computing
- ▶ Emerging computing devices and materials for AI
- ▶ Circuits and Systems for neural network-based language modeling
- ▶ Hardware/software co-design and design automation for AI systems
- ▶ Neuromorphic and bioinspired circuits, processors, systems and their applications
- ▶ Emerging applications: IoT, healthcare, smart factories, environment, robotics, autonomous vehicles, drones, AR/VR, metaverse
- ▶ Computer vision algorithms and architectures
- ▶ AI for Future Wireless Communications
- ▶ AI for Circuits and Systems
- ▶ Compute in Memory

IMPORTANT DATES

- ▶ Special Session Proposal Submission
- ▶ Acceptance notification of Special Session Proposal
- ▶ Submission Deadline (Regular and Special Sessions)
- ▶ Submission Deadline (Live demo papers)
- ▶ Tutorial Proposal Submission
- ▶ Acceptance Notification of Tutorial Proposal
- ▶ Acceptance Notification
- ▶ Submission of Camera-Ready Papers

March 01, 2026

March 15, 2026

March 22, 2026

March 22, 2026

April 23, 2026

May 10, 2026

July 1, 2026

July 10, 2026

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