

ISOCC 2025 22nd International SoC Design Conference

October 15-18, 2025 Paradise Hotel Busan, Busan, Korea

International Organizing Committee

General Chair

• Yong Sin Kim (Korea University, Korea)

General Co-Chairs

• It will be updated soon.

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Conference Management Chairs

• Won-Young Lee (Seoultech, Korea)

V Technical Program Committee

Technical Program Chair

• Minkyu Je (KAIST, Korea)

About ISOCC

ISOCC has established a long tradition as an annual conference providing the premier SoC design forum for worldwide researchers from academia and industries. Since its inception, ISOCC has been continuing to showcase the most recent innovations and trends in the semiconductor system-on-a-chip area with active participations from worldwide researchers in academia, industry, and institutes. ISOCC 2025 welcomes technical papers in the field of semiconductor circuits and systems presenting new advanced concept and developments in analog and digital circuit and system design, theory, simulation, modeling, experimental implementations and experiences, and emerging technologies in the system-on-a-chip area.

The 22nd International SoC Conference (ISOCC 2025) will be held from October 15 to 18, 2025 at the Paradise Hotel Busan, Busan, South Korea. Busan, located on the southeastern coast of the Korean Peninsula, is South Korea's second-largest city and a vibrant hub of culture, tourism, and industry. Known for its stunning beaches, majestic mountains, and historic temples, Busan offers a unique charm with plenty of attractions and activities for visitors to enjoy, making it a delightful city to explore during your stay.

ISOCC 2025 is financially co-sponsored by IEEE CAS Society. All accepted papers will be published in the conference proceedings and will be submitted for inclusion in IEEE Xplore. We also welcome proposals for tutorials and special sessions.

Topics of Interest Topics include, but are not limited to:

- Analog Circuits
- Analog Circuits
- Amplifiers and Filters
- Power Management Circuits

- Machine Learning and AI
- Machine Learning/AI Algorithms
- Systems and Architectures for Machine Learning/AI
- Machine Learning Accelerators and Circuits

Technical Program Co-Chair

• Kea-Tiong (Samuel) Tang (National Tsing Hua University, Taiwan)

Technical Program Vice Chairs

- Junghyup Lee (DGIST, Korea)
- Ji-Hoon Kim (Hanyang University, Korea)

🔽 Secretariat

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🗸 Venue: Paradise Hotel Busan

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- Data Converters
- Analog-to-Digital Converter
- Digital-to-Analog Converters
- Analog Circuits for Data Converters
- RF/Microwave/Wireless
- ${\scriptstyle \bullet}$ RF Circuits and Transceivers
- Microwave and Millimeter-Wave Circuits
- Wireless Communication Circuits
- Wireline
- High-Speed Interface
- Wireline Link
- Digital Circuits, Architecture, and Systems
- $\boldsymbol{\cdot}$ Multimedia Systems and Image Processing Applications
- Digital Signal Processing and Communication Systems
- Embedded Software and Systems
- Hardware Security Circuits
- Memory Circuits and Systems

Important Dates

- Near-Memory and In-Memory Computation for AI
- Neuromorphic Architectures and Processors

- SoC Design Methodology and CAD

- System-Level Modeling and Design Methodology
- Logic/High-Level Synthesis and Optimization
- Physical Design
- Design for Manufacturability and Reliability
- Testing, Validation, Simulation and Verification

- Circuits and Systems for Emerging Technologies

- Neuromorphic and Emerging Devices and Circuits
- Sensory and Biomedical Circuits and Systems
- Automotive Circuits and Systems
- IoT/IoE Circuits and Systems
- Heterogeneous Integration, 2.5D/3D Integration, and Advanced Packaging of Circuit Systems
- Submission of Special Session and Tutorial Proposals: May 23, 2025
- Notification of Acceptance of Special Session and Tutorial Proposals: May 30, 2025
- Submission of Regular Session and Special Session Full Papers: June 13, 2025
- Notification of Acceptance of Regular Session and Special Session Full Papers: August 1, 2025
- Author Registration: August 22, 2025
- Early Registration: August 22, 2025
- Submission of Final Papers (for all accepted papers): August 22, 2025
- On-site Registration: October 15-18, 2025

Paper Submission

A complete 2-page manuscript must be submitted electronically In PDF format (In Standard IEEE double-column format posted on the conference website). Only electronic submissions will be accepted. Accepted papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements. For more information, please refer to the conference website: http://www.isocc.org













