

# 34<sup>th</sup> IFIP/IEEE International Conference on Very Large-Scale Integration

## VLSI-SoC 2026

October 11-14, 2026

Limassol, Cyprus

[www.vlsi-soc.com](http://www.vlsi-soc.com)



VLSI-SoC 2026 is the 34<sup>th</sup> in a series of international conferences sponsored by IFIPTC 10 WG 10.5, IEEE CASS, IEEE CEDA and ACM SIGDA, which explores the state-of-the-art in the areas that surround Very Large Scale Integration (VLSI) and System-on-Chip (SoC). Previous conferences have taken place in Edinburgh, Trondheim, Tokyo, Vancouver, Munich, Grenoble, Gramado, Lisbon, Montpellier, Darmstadt, Perth, Nice, Atlanta, Rhodes, Florianópolis, Madrid, Hong Kong, Santa Cruz, Istanbul, Playa del Carmen, Daejeon, Tallinn, Abu Dhabi, Verona, Cuzco, Salt Lake City, Singapore, Patras, Sharjah, Tangerang, and Puerto Varas. The 2026 edition will take place at Limassol, Cyprus, a tourist and top destination in Southeast Europe.

**Paper Submission:** Papers should present original research and industrial results not published or submitted for publication in other forums. Electronic submission in PDF format to the EasyChair platform for VLSI-SoC 2026 is required. The proceedings will be published by IEEE and will be made available through IEEE Xplore. A selection of the conference best papers will be invited to be submitted to a special issue of the ACM Journal on Emerging Technologies in Computing Systems. Papers from special sessions will be invited to submit an extended version to be included as chapters of a book to be published by Springer.

**Paper Format:** Papers should not exceed 4 pages + 1 page with only references (single-spaced, 2 columns, 10pt font). Submissions should be camera-ready, following the IEEE proceedings specifications.

**Paper Publication and Presenter Registration:** Papers will be accepted for regular or poster presentation at the conference. Every accepted paper MUST have at least one author registered (full rate) at the conference by the time the camera-ready paper is submitted; the registered author is also expected to attend the conference and present the paper. A limited number of travel grants are available to needy PhD students from emerging countries.

**PhD & Student Forum:** A dedicated poster session will be included in the technical program of the main conference. The Best PhD & Student Forum winner will be invited to submit an extended version to be included as chapters of a book to be published by Springer.

### General Co-Chairs:

- Tiziana Margaria (Univ. of Limerick, IE),
- Ian O'Connor (École Centrale de Lyon, FR)

### Program Co-Chairs:

- Panagiota Nikolaou (Univ. of Central Lancashire, CY),
- Florenc Demrozi (Univ. of Stavanger, NO)

### Publication Co-Chairs:

- Enrico Fraccaroli (UNC Chapel Hill, US)
- Christiana Ioannou (University of Central Lancashire, CY)

### Special Session Co-Chairs:

- Maria Michael (University of Cyprus, CY)
- Theocharis Theocharides (University of Cyprus, CY)

### Tutorial Co-Chairs:

- Alberto Bosio (École Centrale de Lyon, FR)
- Rafaella Elia (University of Cyprus, CY)

**Finance Chair:** Anna Philippou (University of Cyprus, CY)

**Industrial Chair:** Leticia B. Pöhls (Leibniz IHP Microelectronics, DE)

**Local Industrial Chair:** Andreas Diavastos (Cyprus Univ. of Technology, CY)

**Publicity Chair:** Nicolò Bellamino (Politecnico di Torino, Italy)



### Topics of interest include (but not limited to) the following:

1. Applications and Architectures for AI and ML
2. Communication Architectures and Technologies
3. Computing Paradigms
4. Digital Circuits Design and Synthesis
5. Embedded System Design
6. Emerging Technologies and Applications
7. Low-Power, Energy-Efficient and Thermal-Aware Design
8. Security
9. Sensing and Signal Processing Technologies
10. Smart Systems, Devices and Applications
11. System Specification, Simulation and Verification
12. Test and Dependability



### Extended:

Abstract Deadline:	<u>04 May 2026</u>
Full Paper Deadline:	<u>11 May 2026</u>
Special Session Deadline:	<u>11 May 2026</u>
Author Notification:	<u>22 June 2026</u>
PhD & Student Forum:	<u>02 July 2026</u>
Camera Ready:	<u>13 July 2026</u>

**Panel Chair:** Sara Vinco (Politecnico di Torino, IT)

### Award Co-Chairs:

- Katell Morin-Allory (TIMA Laboratory, FR)
- George Papadimitriou (University of Patras, GR)
- Mohammad Iman (Université Grenoble Alpes, FR)

### Ph.D. Forum Co-Chairs:

- Fatih Ugurdag (Ozyegin Univ., TR)
- Nahla El-Araby (TU Wein, AT)

**Local Arrangements:** Nicolas Stratis, Petros Stratis (EasyConferences, CY)

**Web Chair:** Matteo Iervasi (Univ. of Stavanger, NO)

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## Confirmed Keynote Speakers



Prof. **Valeria Bertacco**  
University of Michigan, USA

**TBA**

**Short Abstract:** TBA



Prof. **Giovanni De Micheli**  
EPFL, Switzerland

**Sustainable and Secure Computing: the Last Frontier**

**Short Abstract:** This keynote explores the convergence of sustainability and security in modern computing, highlighting key challenges and emerging approaches for building energy-efficient and resilient systems.



Prof. **Freddy Gabbay**  
Hebrew Univ. of Jerusalem

**Do We Really Need All the Bits? Value-Driven Approximate Computing for AI Accelerators**

**Short Abstract:** This keynote explores value-driven approximate computing for AI accelerators, using selective precision and co-design techniques to improve efficiency while preserving model accuracy.

## Confirmed Industrial Keynote Speakers



Dr. **Victor Grimblatt**  
Synopsys, Vitacura, Chile

**Low Power IC Design for a Sustainable World**

**Short Abstract:** This keynote explores key low-power IC design techniques and their role in reducing energy consumption, enabling more sustainable and efficient electronic systems.



Prof. **Wolfgang Ecker**  
TUM- Infineon, Germany

**What is the IP Reuse Trap for Digital Hardware, and how can it be escaped?**

**Short Abstract:** This keynote explores the challenges of IP reuse in digital design and highlights emerging approaches to improve scalability and efficiency in SoC development.



**Odysseas Economides**  
HardwareX Engineering, Cyprus

**Bridging the gap between research and industry through embedded systems**

**Short Abstract:** This keynote explores how embedded systems enable the translation of research innovations into real-world industrial applications, highlighting challenges and strategies for effective technology transfer.

## Further Opportunities

**Call for Special Sessions**

**PhD & Student Forum**

**Two Exciting Tutorial Sessions**

**Inspiring Panel Session**



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