

IEEE CAS Outreach ID#1274 (International Outreach for Nanoelectronic and Information Systems)

- Event
IEEE International Symposium on Nanoelectronic and Information Systems (IEEE-iNIS) 2015 took place from December 21-23rd, 2015 at Hotel Radisson Blu, Indore in the state of Madhya Pradesh, India. The main objective of iNIS is to provide a platform for both hardware and software researchers to interact under one umbrella for design of efficient and secure information processing circuits and systems. The iNIS 2015 had a total of 72 registrant participants. The iNIS 2015 saw a very rich and diverse participation from industry as well as academia, ranging from premier local academic institutes of India to international participation.
- Keynote Speeches
 - (1) Dharma P. Agrawal, Center for Distributed and Mobile Computing, EECS Department University of Cincinnati. Talk on “**Managing Large Healthcare Data**”.
 - (2) Michel Renovell, Robotics and Microelectronics of Montpellier (LIRMM) - University of Montpellier, Montpellier France. Talk on “**Spot Defect Modeling: History and Perspectives**”.
 - (3) Mircea Stan, Department of Electrical and Computer Engineering, University of Virginia. Talk on “**Solving Big-Data Problems with Automata Computing**”.
 - (4) Sandip Kundu, Electrical and Computer Engineering, University of Massachusetts, Amherst, USA. Talk on “**Improving Yield and Reliability of Chip Multiprocessors in Nanotechnology**”.
- Student Travel Grants:

Name of the Student	Title of Paper
Nalesh S.	Energy Aware Synthesis of Application Kernels expressed in Functional Languages on a Coarse Grained Composable Reconfigurable Array
Soumyajit Poddar	Multicore ICs: Recent Trends in Developing Methodologies and Frameworks for Simulation
Wazir Singh	Energy Efficient Analog-to-Information Converter For Biopotential Acquisition Systems
N Prasad	ZMesh: An Energy-Efficient Network-onChip Topology for Constant-Geometry Algorithms
V Ramesh Kumar	Accurate Numerical Model for Crosstalk Analysis of SWCNT Bundle Interconnects using FDTD Method

- Best Paper Awards

Author Names	Title of Paper
Sauvagya Sahoo, Sudeendra Kumar, Kamala Kanta Mahapatra	Modified Configurable RO PUF with Improved Security Metrics
Sreeja Nair, Mary Regeena	FinFETs and their application as load switches in micromechatronics
Kanchan Cecil, Jawar Singh	Performance Enhancement of Dopingless Tunnel-FET based on Ge-source with High-k Dielectric

- Group Pics



A Group Photograph on Day 1



A Group Photograph on Day 2